

This Page Is Inserted by IFW Operations
and is not a part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

IMAGES ARE BEST AVAILABLE COPY.

**As rescanning documents *will not* correct images,
please do not report the images to the
Image Problem Mailbox.**

Docket No.: M4065.0546/P546-A
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Ebrahim Abedifard et al.

Application No.: Not Yet Assigned

Filed: Concurrently Herewith

Art Unit: N/A

For: FLASH MEMORY DEVICE WITH
DISTRIBUTED COUPLING BETWEEN
ARRAY GROUND AND
SUBSTRATE

Examiner: Not Yet Assigned

SUBMISSION OF FORMAL DRAWINGS

MS Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Submitted herewith is one set (8 sheets, figures 1-8) of formal drawings for filing in the above-identified patent application. Kindly substitute the enclosed formal drawings for the informal drawings submitted with the originally filed application.

Dated: 16 JAN 2004

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Michael Bergman

Registration No.: 42,318

DICKSTEIN SHAPIRO MORIN &
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant

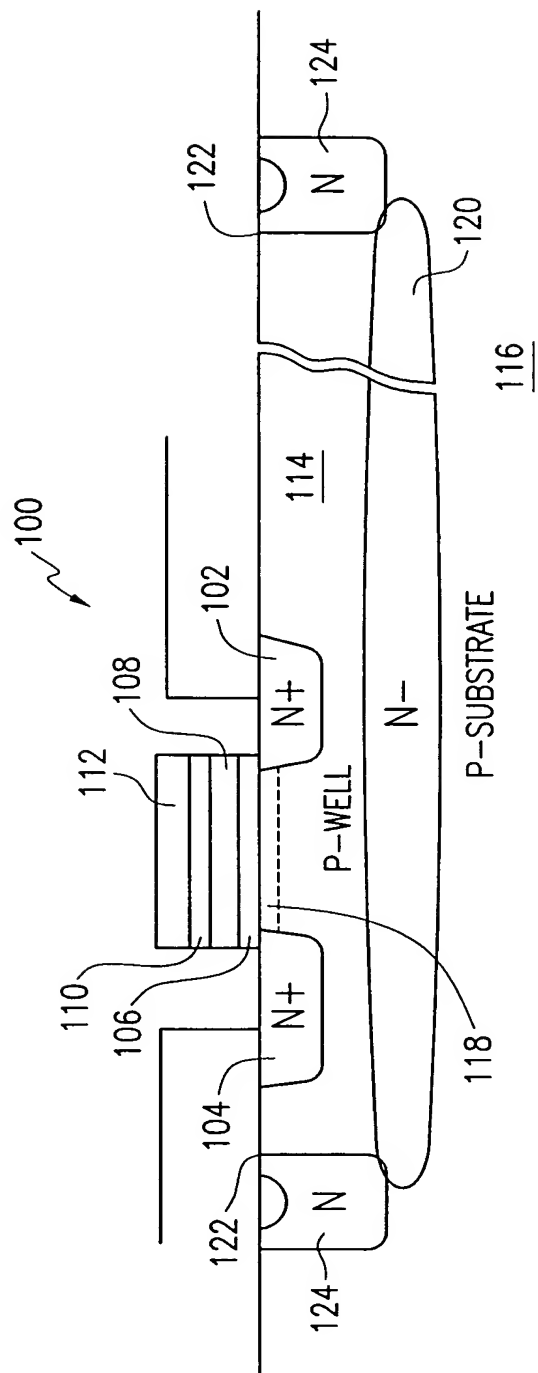


FIG. 1



FIG. 2

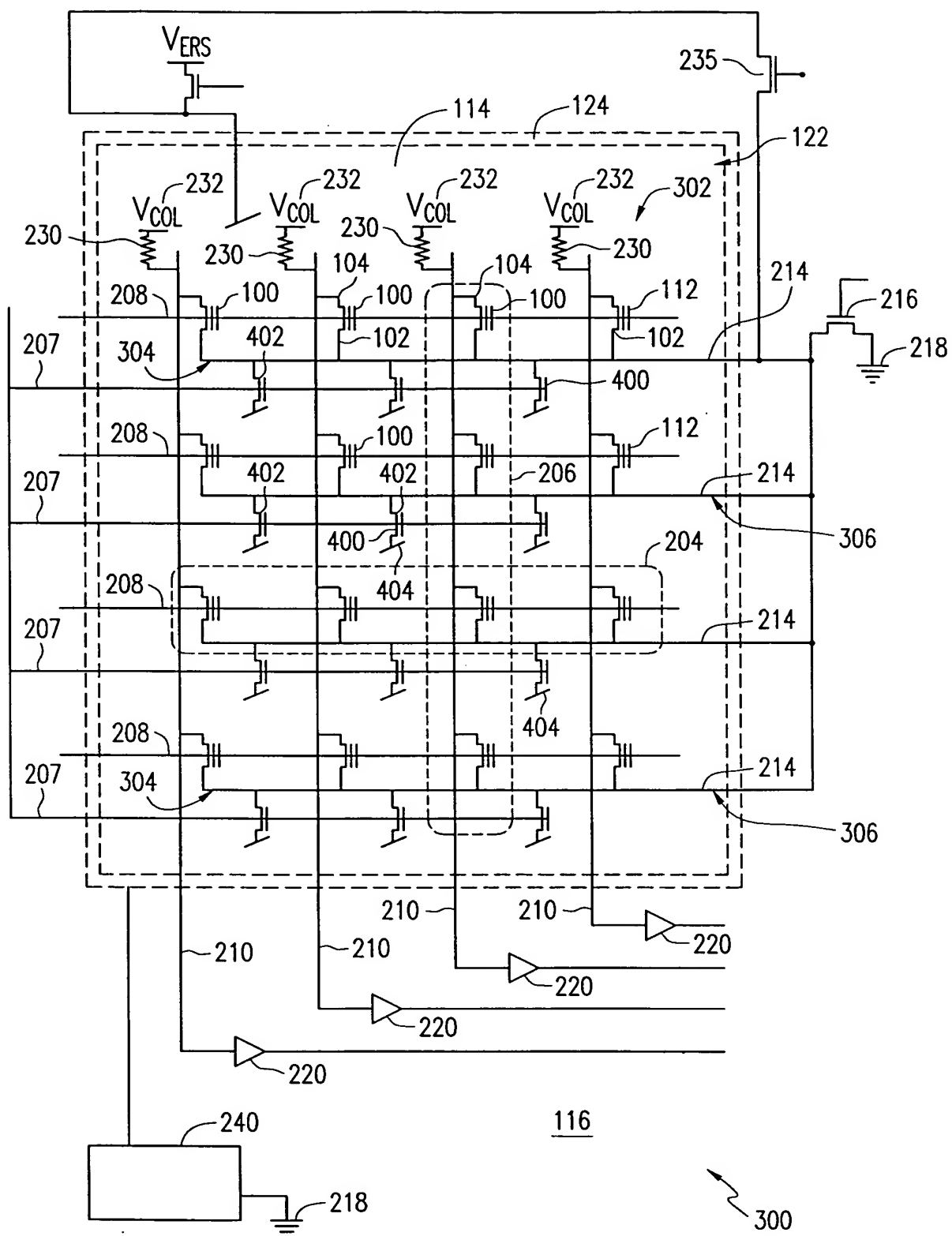


FIG. 3

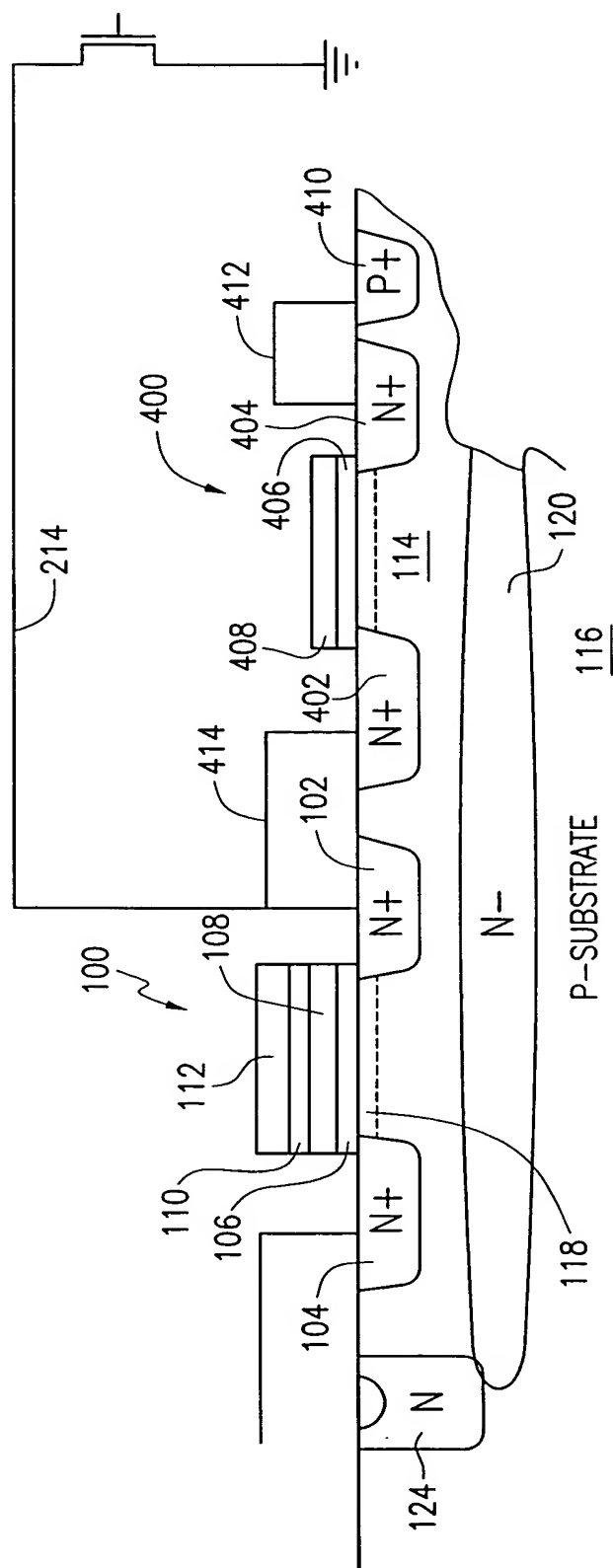


FIG. 4

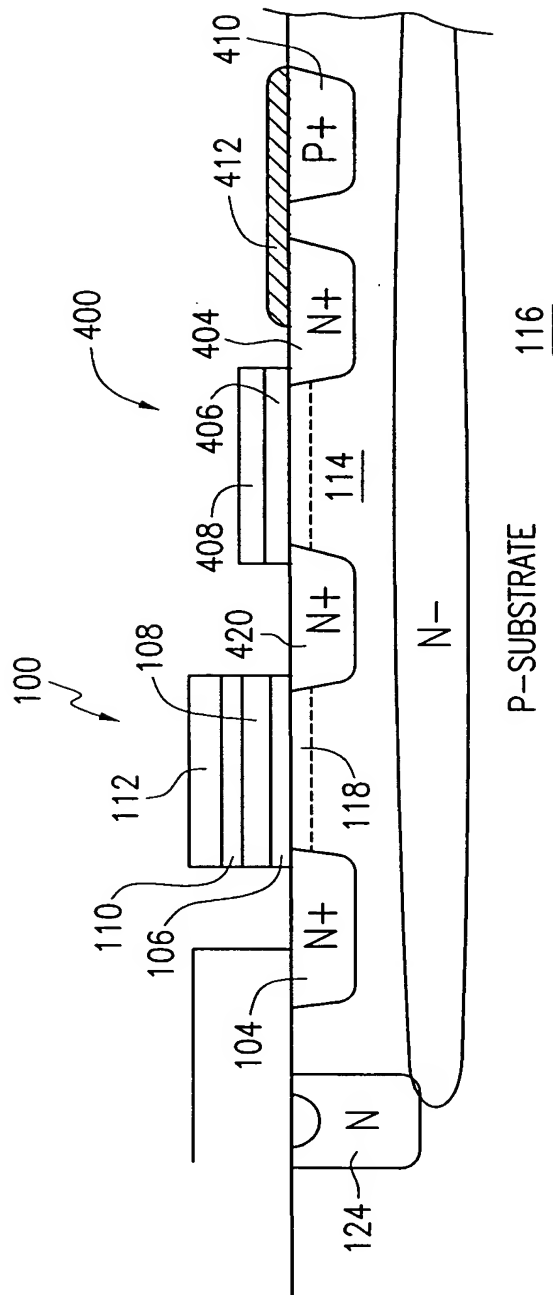


FIG. 5

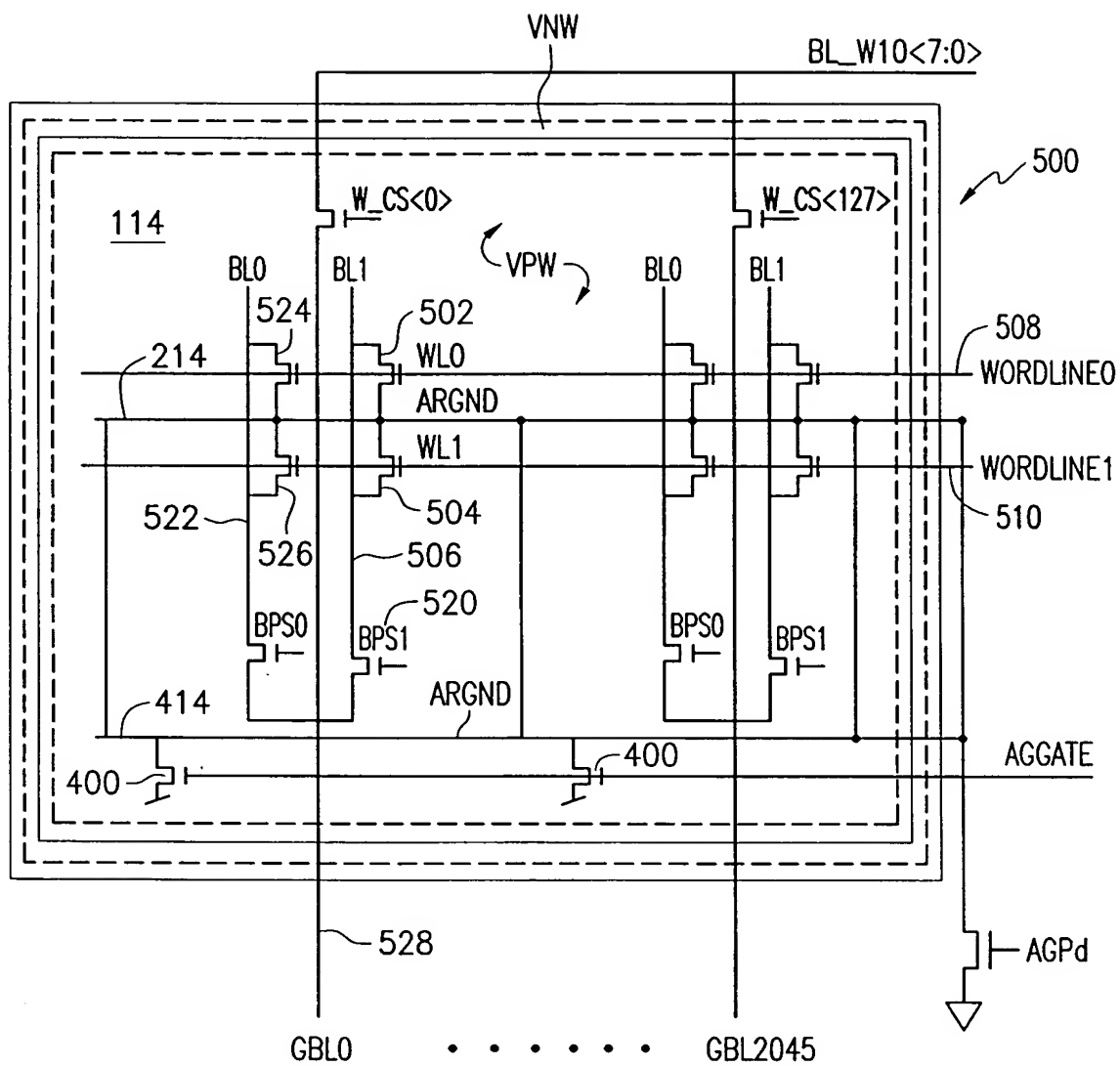


FIG. 6

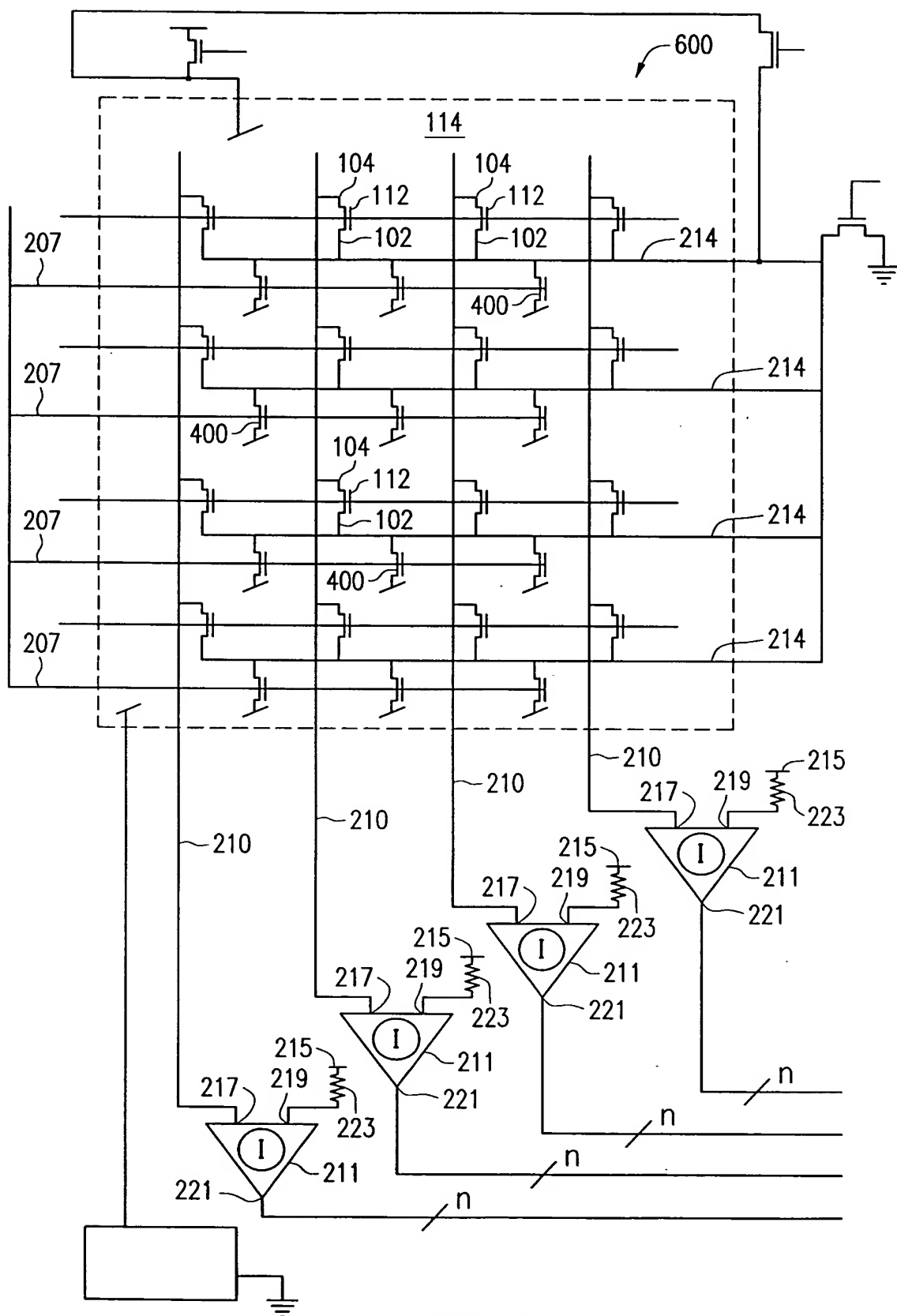


FIG. 7

FIG. 8

